Interference Search justes

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	7	(((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)).clm.	US-PGPUB	OR	ON.	2008/01/04 13:12
L5	7	(((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)).clm.	US-PGPUB	OR .	ON	2008/01/04 13:13
L6	3	((DMA (direct near3 memory near3 access)) near3 (on within on?board) near3 (memory near3 (module))).clm.	US-PGPUB	OR	ON	2008/01/04 13:14

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	68	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L2	676	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) near3 (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L3	103809	(switch hub router) near5 (port interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L4	272	.L2 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L5	52	L4 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
S1	2	"20030065836".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S2	60	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:50
S3	6	(hub near3 port near3 architecture).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:38
S4	12648	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:50

S5	1306	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) with (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:51
S6	435	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) near3 (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:52
S7	78463	(switch hub router) near5 (port interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:52
S8	167	S6 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:53
S9	23	S8 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S10	5362	(DMA (direct near3 memory near3 access)) with (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S11	3477	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S12	588	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON ·	2006/01/06 13:17
S13	29	(DMA (direct near3 memory near3 access)) near3 (on within integrated ob?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:18
S14	28	(DMA (direct near3 memory near3 access)) near3 (on within ob?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:18

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S15	28	(DMA (direct near3 memory near3 access)) near3 (on within on?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:19
S16	588	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:19
S17	380	(DMA) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:19
S18	285	S17 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S19	57	DMA with (link near3 list)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S20	281865	ed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S21	137	DMA with (linked near3 list)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S22	113	S21 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S23	36	dma with (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:02
S24	1	(memory near3 hub) with (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04

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S25	1	(memory near3 hub) same (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04
S26	25	(memory near3 hub) and (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04
S27	162	DMA with (next near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S28	128	S27 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ,	ON	2006/01/07 10:43
S29	2	"20040216018".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:06
S30	284	(dma near3 controller) with ((complet\$4 status error) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:17
S31	6109	(DMA (direct near3 memory near3 access)) with (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S32	136	S30 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S33	48	S32 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:21
S34	9	(dma near3 controller) with ((complet\$4 and (status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:23

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S35	3	S34 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:40
S36	238	(dma near3 controller) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:05
S38	73	S36 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:26
S39	951	(dma with (((status error)) near3 (register)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S40	71	(dma with (((status and error)) near3 (register)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:40
S41	20	S40 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S42	7	(dma with (((status error)) near3 (register))) same (memory near3 module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S43	2	S42 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 13:31
S44	238	(dma near3 controller) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 13:35
S45	73	S44 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 13:36

S46	352	(dma near3 (controller channel engine)) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:22
S47	107	S46 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:23
S48	20	(dma near3 controller) with (((error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:06
S49	30	(dma near3 (controller channel engine)) with (((error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S50	7	S49 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S51	333	(dma with error with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S52	97	S51 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	68	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L2	676	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) near3 (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L3	103809	(switch hub router) near5 (port interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L4	272	L2 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
L5	52	L4 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:22
S1	12	"20030065836".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S2	60	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router) with (dma)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:50
S3	6	(hub near3 port near3 architecture).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:38
S4	12648	((plurality (first and second) multiple) near3 (memory buffer register)) with (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:50

S5	1306	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) with (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/01/06 09:51
S6	435	((plurality (first and second) multiple) near3 (memory) near3 (device module chip)) near3 (switch hub router)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:52
S7	78463	(switch hub router) near5 (port interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:52
S8	167	S6 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:53
S9	23	S8 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S10	5362	(DMA (direct near3 memory near3 access)) with (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S11	3477	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:16
S12	588	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/01/06 13:17
S13	29	(DMA (direct near3 memory near3 access)) near3 (on within integrated ob?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:18
S14	28	(DMA (direct near3 memory near3 access)) near3 (on within ob?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:18

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S15	28	(DMA (direct near3 memory near3 access)) near3 (on within on?board) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:19
S16	588	(DMA (direct near3 memory near3 access)) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:19
S17 _.	380	(DMA) near3 (memory near3 (module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR [·]	ON	2006/01/06 13:19
S18	285	S17 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S19	57	DMA with (link near3 list)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S20	281865	ed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 09:53
S21	137	DMA with (linked near3 list)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S22	113	S21 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OŅ	2006/01/07 10:43
S23	36	dma with (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:02
S24	1	(memory near3 hub) with (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04

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S25	1	(memory near3 hub) same (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04
S26	25	(memory near3 hub) and (crossbar near3 switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:04
S27	162	DMA with (next near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S28	128	S27 and @ad<="20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/07 10:43
S29	2	"20040216018".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:06
S30	284	(dma near3 controller) with ((complet\$4 status error) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:17
S31	6109	(DMA (direct near3 memory near3 access)) with (memory near3 (module device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S32	136	S30 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:10
S33	48	S32 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:21
S34	9	(dma near3 controller) with ((complet\$4 and (status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:23

S35	3	S34 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:40
S36	238	(dma near3 controller) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:05
S38	73	S36 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:26
S39	951	(dma with (((status error)) near3 (register)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S40	71	(dma with (((status and error)) near3 (register)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:40
S41	20	S40 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S42	7	(dma with (((status error)) near3 (register))) same (memory near3 module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 11:41
S43	2	S42 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 13:31
S44	238	(dma near3 controller) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ,	ON	2007/01/29 13:35
S45	73	S44 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 13:36

S46	352	(dma near3 (controller channel engine)) with (((status error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:22
S47	107	S46 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:23
S48	20	(dma near3 controller) with (((error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:06
S49	30	(dma near3 (controller channel engine)) with (((error)) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S50		S49 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S51 ·	333	(dma with error with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25
S52	97	S51 and (@ad<="20030722" and @rlad<="20030722")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/29 14:25